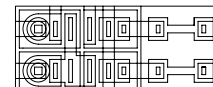


PARAMETERS:
VBIAS 0

```

*****
.SUBCKT Y5U PLUS MINUS PARAMS: C=1nF
** Generated by GetData Graph Digitizer 2.24 on May 13 2013, 08:38
** from file ...Capacitor_Voltage_Change_Data_Formatted.txt
C_C1      PLUS INT1 {C}
V_VM      INT1 MINUS
G_G1      PLUS MINUS VALUE { ((V(INT2, MINUS))-1)*(V_VM) }
R_E_E1    INT2 MINUS 1G
E_E1      INT2 MINUS TABLE { abs(V(PLUS, MINUS)) }
+ (0,1
+ 0.9765,0.997
+ 1.952,0.9809
+ 3.0567,0.95
+ 4.096,0.91324
+ 5.395,0.8618
+ 6.433,0.81332
+ 7.602,0.7604
+ 8.705,0.709
+ 9.679,0.66785
+ 10.588,0.6355
+ 11.822,0.5856
+ 12.991,0.5488
+ 15.396,0.4783
+ 16.89,0.44
+ 18.32,0.4121
+ 20.4,0.3739
+ 24.11,0.3253
+ 27.753,0.2855
+ 30.226,0.2634
+ 34.455,0.2339
+ 39.467,0.2059
+ 44.153,0.1852
+ 50.2,0.163)
.ENDS Y5U
*****

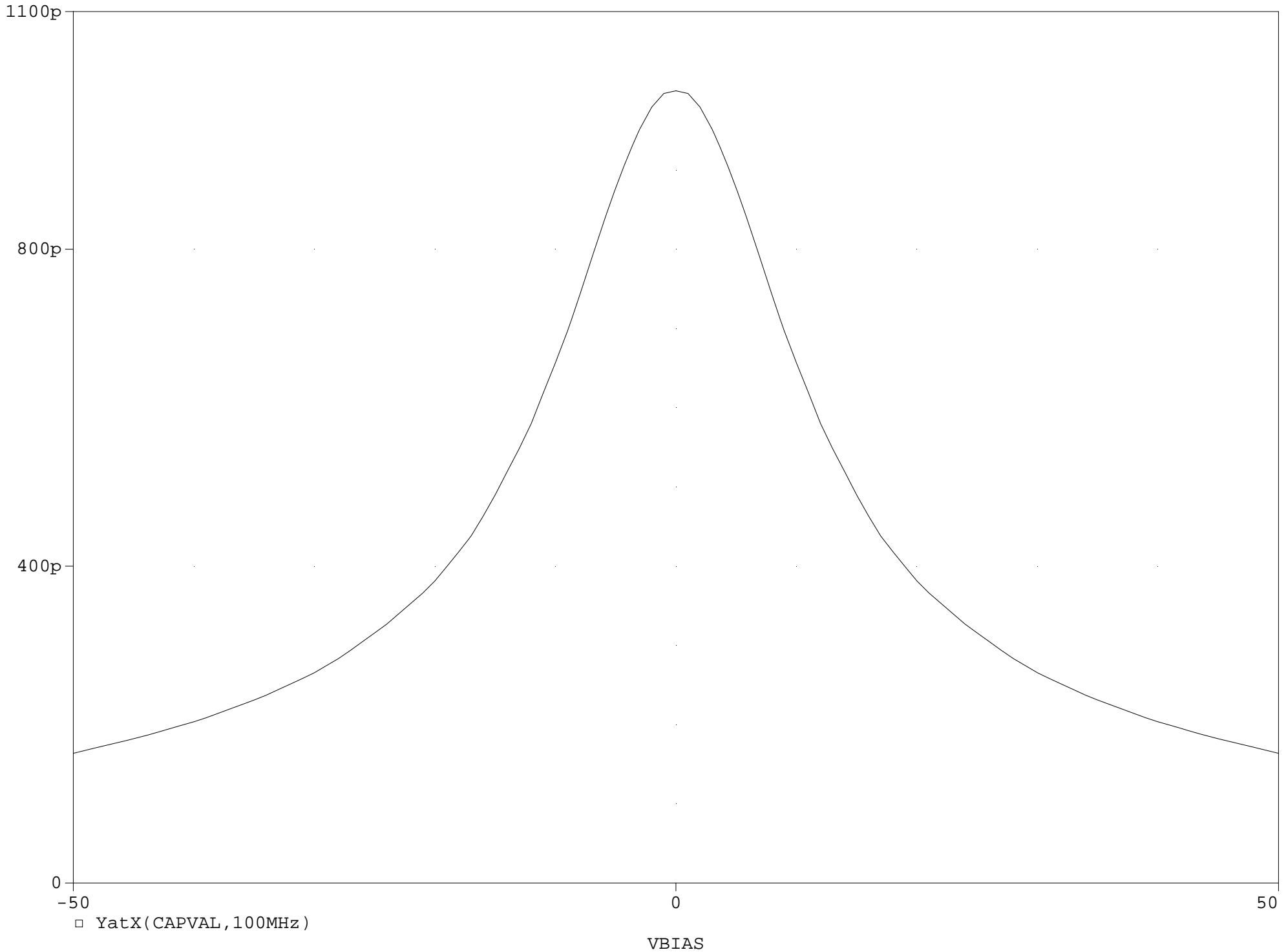
```



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Title: Making a Capacitor behave with a voltage-sensitivity like Y5U		
Size A	FileName: C:\Projects\Expments\SED\CapacitorVersusVoltage.sch	REV A
May 13, 2013, 5:01 PM		Sheet 1 of 1

Capacitor Versus Voltage, Y5U



□ YatX(CAPVAL,100MHz)