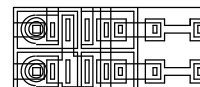


WHEN INPUT=LOW, InverterVDD = +5V

WHEN INPUT=HIGH, InverterVDD = +3.3V



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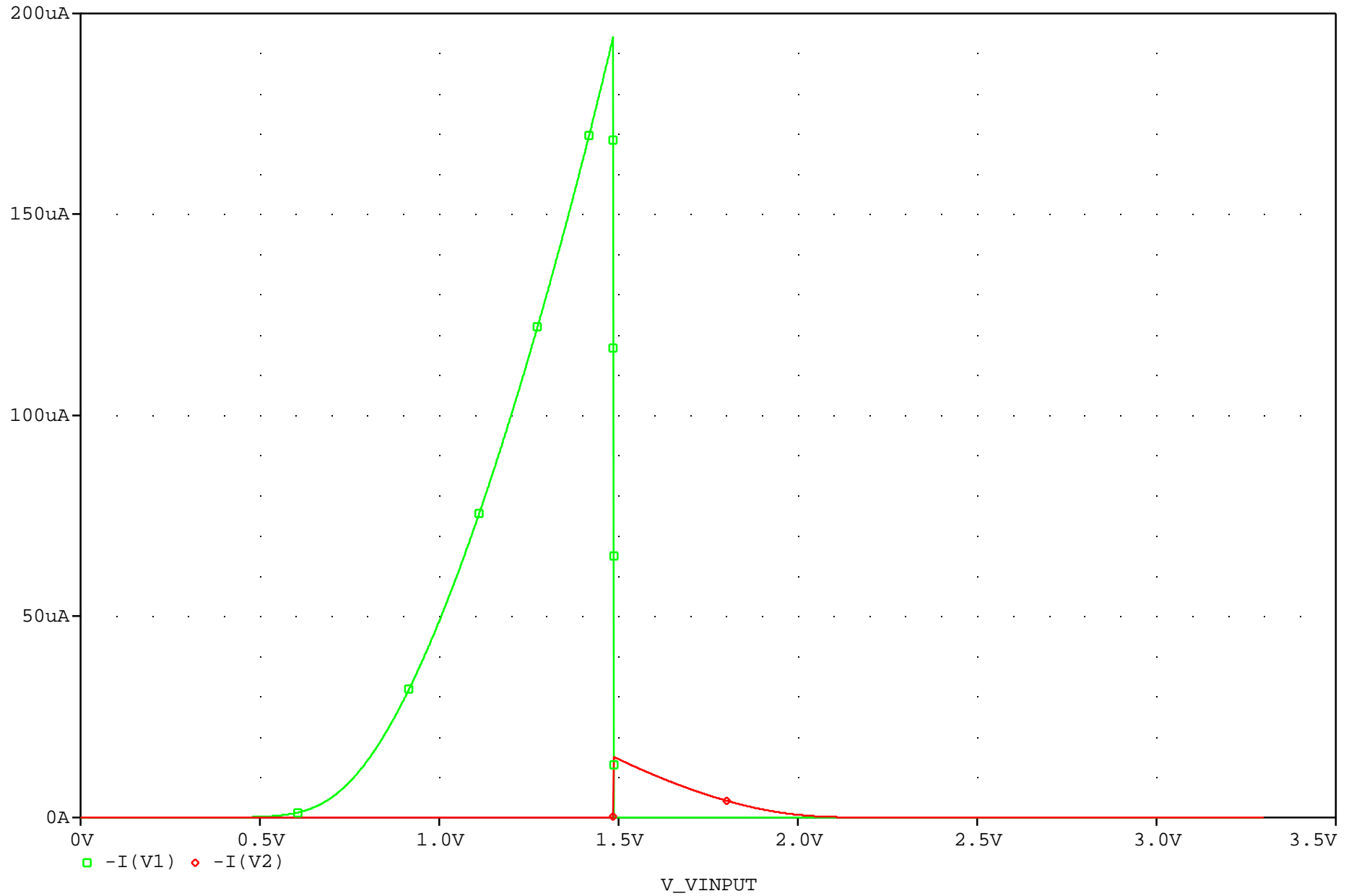
Title:
 Logic Translator - Conceptual

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 D:\...SED\Examples\LogicTranslator-Conceptual.sch REV A

October 31, 2005, 4:43 PM

Sheet 1 of 1

LogicTranslator-Conceptual - Input Sweep VINPUT Increasing



LogicTranslator-Conceptual - Input Sweep VINPUT Decreasing

