

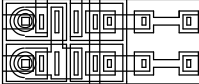
This circuit shows the bare basics of creating an ideal diode characteristic using back-to-back power FETS plus a controlling comparator (or comparators).

In my actual Lilon charger design there were multiple sensing circuits that defined which direction current was allowed to flow, and how much (analog controls for allowed charging current, discharge current limiting, and discharge stop), all based on cell voltages.

Reference was an on-chip BandGap.

Process was HV CMOS.

PARAMETERS:
VDD 3.3V

		ANALOG INNOVATIONS, Inc. 824 E. CATHEDRAL ROCK DRIVE PHOENIX, AZ 85048-6300 (480) 460-2350 FAX: (480) 460-2142	
Title: Perfect Diode for Charger Isolation			
Size A	FileName: ...PerfectDiodeForChargerIsolation.sch		REV A
February 26, 2004, 3:08 PM		Sheet 1 of 1	

PerfectDiodeForChargerIsolation

